

PATENT NUMBER

986 1  
U.S. PTO

09/1842332

04/24/01

ISSUE CLASSIFICATION	
Class	Subclass

## U.S. UTILITY Patent Application

**O.I.P.E.**

**PATENT DATE**

**SCANNED**

**Q.A**

APPLICATION NO. 09/842332	CONT/PRIOR	CLASS 375	SUBCLASS 354	ART UNIT 2631+	EXAMINER TSE HA
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## APPLICANTS

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**title**

Source: synchronous receiver link initialization and input floating control by clock detection and DLL lock detection

PTO-2040  
12/89

ISSUING CLASSIFICATION															
ORIGINAL				CROSS REFERENCE(S)											
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)										
INTERNATIONAL CLASSIFICATION															

☐ Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
	_____ (Primary Examiner) (Date)		<b>ISSUE FEE</b>	
Amount Due			Date Paid	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Legal Instruments Examiner) (Date)		<b>ISSUE BATCH NUMBER</b>	
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